

cancel
A

Application Serial No. 09/327,756 filed June 7, 1999[, which is a Continuation-in-Part of Application Serial No. 09/305,896 filed May 5, 1999, which is a Continuation-in-Part of copending Application No. 09/275,518 filed March 24, 1999, which is a Continuation-in-Part of copending Application Nos.: 09/274,265 filed March 22, 1999; 09/243,078 filed February 2, 1999; 09/241,930 filed February 2, 1999; 09/157,778 filed September 21, 1998; 09/047,146 filed March 24, 1998, 08/949,915 filed October 14, 1997, now U.S. Letters Patent 6,158,659; 08/854,832 filed May 12, 1997, now U.S. Letters Patent 6,085,978; 08/886,806 filed April 22, 1997, now U.S. Letters Patent 5,984,185; 08/726,522 filed October 7, 1996, now U.S. Letters Patent 6,073,846; 08/573,949 filed December 18, 1995, now abandoned]; each said application being commonly owned by Assignee, Metrologic Instruments, Inc., of Blackwood, New Jersey, and incorporated herein by reference as if fully set forth herein.

AMENDMENT OF THE CLAIMS TO INVENTION

Please cancel Claims 1-262 without prejudice or disclaimer and add Claims 263-277 as follows:

--263. A planar laser illumination and imaging module (PLIIM) realized on a semiconductor chip comprising: a pair of micro-sized (diffractive or refractive) cylindrical lens arrays mounted upon a pair of large linear arrays of surface emitting lasers (SELs) fabricated on opposite sides of a linear electronic image detection array. --

--264. A PLIIM-based semiconductor chip comprising:

a pair of linear SEL arrays for producing a composite planar laser illumination beam;

a linear electronic image detection array having field of view (FOV) arranged in a coplanar relationship with said composite planar laser illumination beam, wherein said linear electronic image detection array and said pair of linear SEL arrays are each formed a common semiconductor substrate so that said linear electronic image detection array is arranged between said pair of linear SEL arrays; and

an integrated circuit package encasing said linear electronic image detection array and said pair of linear SEL arrays, said integrated circuit package having

electrical connector pins for connected to a host system,

first and second elongated light transmission windows disposed over said pair of linear SEL arrays so that said composite planar laser illumination beam , and